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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/717,517	11/21/2003	Jae-Hee Cho	030681-595	8406
21839	7590	05/18/2005	EXAMINER	
BURNS DOANE SWECKER & MATHIS L L P POST OFFICE BOX 1404 ALEXANDRIA, VA 22313-1404				ROSE, KIESHA L
ART UNIT		PAPER NUMBER		
				2822

DATE MAILED: 05/18/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/717,517	CHO ET AL	
	Examiner Kiesha L. Rose	Art Unit 2822	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 21 November 2003.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-18 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____. |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>11/21/03</u> . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____. |

DETAILED ACTION

This Office Action is in response to the filing of the application.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-6,8,10 and 12-17 are rejected under 35 U.S.C. 102(e) as anticipated by or, in the alternative under 35 U.S.C. 103(a) as obvious over Uemura (U.S. Patent 6,794,690).

Uemura discloses a light emitting diode (Fig. 5) that contains a substrate (101) on which an n-type semiconductor layer (102), an active layer (103) and a p-type semiconductor layer (104) are sequentially stacked, a p-type electrode, which includes a first metallic layer (111) formed on the p-type semiconductor layer and a second metallic layer (112) that is formed on the first metallic layer and reflects light generated from the active layer, wherein the first metallic layer has a contact resistance with the p-type semiconductor layer lower than that of the second metallic where the second metallic layer has a light reflectivity higher than that of the first metallic layer

The first metallic layer is formed of platinum (pt) or palladium (pd) and has thickness between 1nm and 10nm (Column 4, lines 51-56 and Column 11, lines 46-50)

The second metallic layer is formed of aluminum (Al) with a thickness of more than 50nm (Column 11, lines 20-23 and 57-62)

The n-type semiconductor layer, active layer and p-type semiconductor layer are GaN based III-V nitride compound

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 7 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Uemura.

Uemura discloses all the limitations except for the first and second metallic layers to be thermally processed at a temperature between 80-350 degrees. Whereas Uemura discloses the first and second metallic layers to be heated in a nitrogen atmosphere (nonoxygen) processed at a temperature of 400 degrees. (Column 7, lines 59-61 and Column 9, lines 25-41) In addition, It would have been obvious to one having ordinary skill in the art at the time the invention was made to have the first and second metallic layers to be processed at temperatures ranging between 80-350 degrees, since it has been held that where the general condition of a claim are disclosed in the prior

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art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 USPQ 233. (1955) In regards claim 7 referring to the first and second metallic layers being thermally processed in an nonoxygen atmosphere, a "*product by process*" claim is directed to the product per se, no matter how actually made, *In re Hirao and Sato et al.*, 190 USPQ 15 at 17 (CCPA 1976) (footnote 3). See also *In re Brown and Saffer*, 173 USPQ 685 (CCPA 1972); *In re Luck and Gainer*, 177 USPQ 523 (CCPA 1973); *In re Fessmann*, 180 USPQ 324 (CCPA 1974); and *In re Marosi et al.*, 218 USPQ 289 (CAFC 1983) final product per se which must be determined in a "*product by, all of*" claim, and not the patentability of the process, and that an old or obvious product, whether claimed in "*product by process*" claims or not. Note that Applicant has the burden of proof in such cases, as the above caselaw makes clear. Even though product –by [-] process claims are limited by and defined by the process, determination of patentability is based upon the product itself. The patentability of a product does not depend on its method of production. If the product in product-by-process claim is the same as or obvious from a product of the prior art, the claim is unpatentable even though the prior product is made by a different process." *In re Thorpe*, 227 USPQ 964, 966 (Fed. Cir. 1985)(citations omitted)."

Claims 9 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Uemura.

Uemura discloses the claimed invention except for the active layer to be an n-type material layer $\text{In}_x\text{Al}_y\text{Ga}_{1-x-y}\text{N}$ ($0 \leq x \leq 1$, $0 \leq y \leq 1$, and $x + y \leq 1$) based n-type material. Whereas Uemura disclose the active layer to be a n-type material layer of

$\text{In}_{1-x-y} \text{Al}_x \text{Ga}_y \text{N}$ ($0 \leq x \leq 1$, $0 \leq y \leq 1$, and $x + y \leq 1$) (Column 3, lines 20-30). The Uemura reference discloses different examples of ratios that can be used to form the active layers. It would have been obvious to one of ordinary skill in the art at the time the invention was made to for the active layer of $\text{In}_x \text{Al}_y \text{Ga}_{1-x-y} \text{N}$ ($0 \leq x \leq 1$, $0 \leq y \leq 1$, and $x + y \leq 1$), since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 USPQ 233 (1955)

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kiesha L. Rose whose telephone number is 571-272-1844. The examiner can normally be reached on M-F 8:30-6:00 off 2nd Mondays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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KLR



AMIR ZARABIAN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2809